PWM Method to Analysis the Design of a Novel 27 level Three-Phase Buck-Boost Converter

Akansh Gupta[1] Shobhna Jain[2]
[1]Student [2]Professor
[1,2]University Institute of Technology, RGPV, Bhopal (MP), India

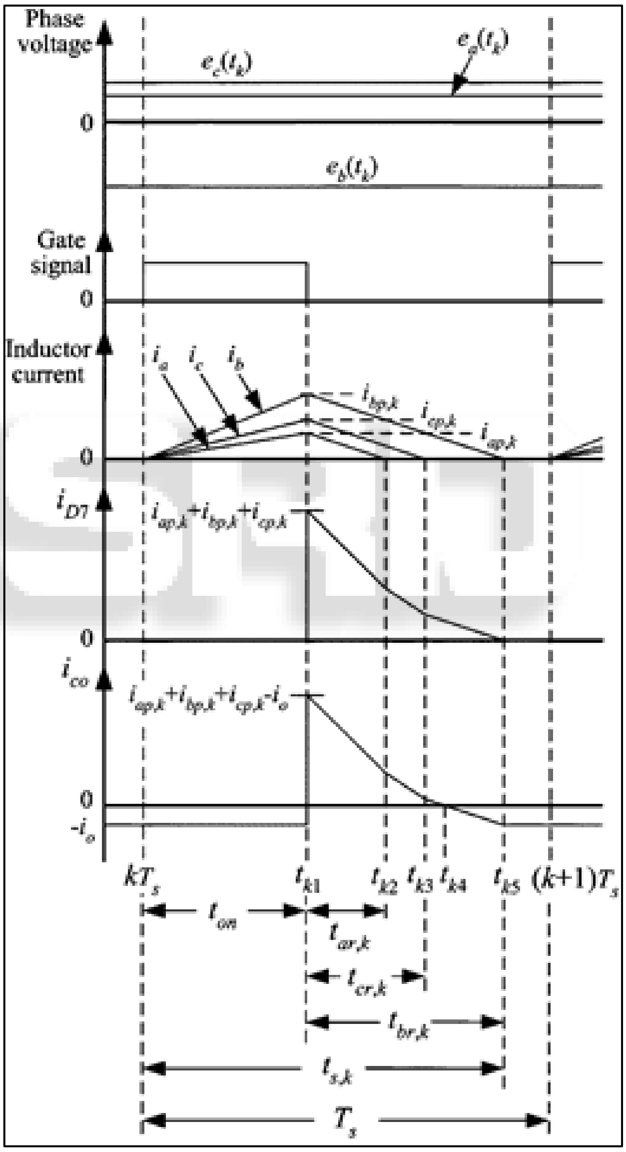
Abstract— This paper proposes a singular three-segment ac–dc buck- raise converter. The proposed converter makes use of 4 energetic switches, which can be pushed with the aid of using most effective one manage signal. This converter is operated in discontinuous conduction mode (DCM) with the aid of using the usage of the pulsewidth modulation (PWM) technique, and the manage scheme very effortlessly and truly achieves only sinusoidal enter current, excessive energy factor, low overall harmonic distortion of the enter current THD. It gives a unification of buck-raise and fly lower back converter for using a cascaded H-bridge multilevel inverter with a single unbiased DC voltage supply. Cascaded H-bridge multilevel inverter is beneficial to make many output voltage stages for sinusoidal waveform with the aid of using combining or greater H-bridge modules. However, every H-bridge module desires an unbiased DC voltage supply to generate multi stages in an output voltage. This topological function brings a demerit of growing the variety of unbiased DC voltage reassets whilst it desires to growth the variety of output voltage stages. To resolve this problem, we endorse a converter combining a buck-raise converter with a fly back converter. The proposed converter gives unbiased DC voltage reassets at lower back-quit H-bridge modules. After reading theoretical operation of the circuit topology, the validity of the proposed method is proven with the aid of using computer-aided simulations the usage of PSIM and experiments.

Keywords: SMC (sliding mode control), PI and PID Control, DC-DC BUCK converter

I. Introduction

Cascaded H-bridge multilevel inverter has benefits in wide variety of components, excessive reliability, and simplicity. The number one motive of the multilevel inverter is the manufacturing of better output voltage via way of means of synthesizing low voltage of numerous inverter modules linked in series. If the wide variety of module increases, it generates plenty of voltage degrees led to enhancing overall harmonic distortion of output voltage. However, it effects an boom of machine value and complexity troubles because of the huge wide variety of switching gadgets and different components. To remedy the above cited problem, numerous researches applied and reported. Among them, cascaded-transformer primarily based totally multilevel inverter appears to be the exceptional desire to lessen the wide variety of switches and input Synthesizes 27 ranges with inside the output voltage. It includes an unbiased DC voltage source, 3 H-bridge cells related to a cascaded transformer. Because the running frequency of the transformer is low, the scale and extent might be increased. Moreover, the transformer will lower the device efficiency. So we are able to be aware that the usage of low frequency transformer is not the best way to improve Performance of unmarried DC voltage sourced multilevel inverter.

To clear up the problem, we endorse a converter combining a buck-enhance converter with a fly lower back converter. It specializes in minimizing of the unbiased DC enter voltage reasserts in a conventional cascaded H-bridge multilevel inverter.



Some typical waveforms in one switching period during time interval 0-30.

II. Classification of Power Semiconductor Switches

- Power gadgets is split into phrases in their quantity of terminals
  - The two-terminal gadgets (diodes), whose kingdom is absolutely depending on the outside electricity circuit they're linked to;

  - The three-terminal gadgets, whose kingdom isn't always simplest depending on their outside electricity circuit, however additionally at the sign on their riding terminal (gate or base).
- A 2d type has to do with the form of fee vendors they use:–Some gadgets are majority service gadgets (Schottky diode, MOSFET, JFET) - use simplest one form of fee vendors (i.E., either electrons or holes)
  - Others are minority service gadgets (p-n diode, Thyristor, BJT, IGBT) - use each fee vendors (i.E. Electrons and holes).
  - A 0.33 type is primarily based totally at the diploma of controllability: uncontrollable switches (diodes), semi-controllable switches (thyristors), and fully-controllable switches (BJT, MOSFET, JFET, IGBT, GTO, MCT)

III. POWER TRANSISTORS

Power transistors are of 4 types:
1) BJT
2) Power MOSFET
3) IGBT
4) SITs

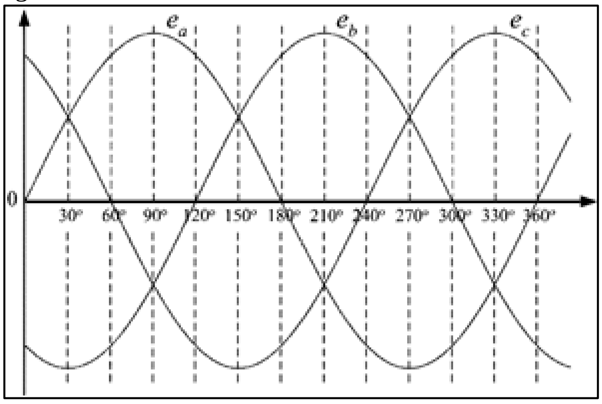
A bipolar transistor has 3 terminals: base, emitter and collector. It is generally operated as a transfer withinside the common –emitter configuration .As lengthy because the base of an NPN transistor is at a better capacity than the emitter and the bottom cutting-edge is adequately big to pressure the transistor withinside the saturation region, the transistor stays on, supplied that the collector –to –emitter junction is nicely biased. High energy bipolar junction transistors are normally utilized in energy converters at a frequency under 10 KHZ and efficiently implemented withinside the energy scores as much as 1200V, 400A.

Power MOSFET are utilized in excessive velocity energy converters and are to be had at a quite low energy score withinside the variety of 1000v, 100A at a frequency variety of numerous tens of kilohertz.

IGBTs are voltage managed energy transistors. They are inherently quicker than BJTs, however nevertheless now no longer pretty as a quick as MOSFETs. but they gives some distance advanced pressure and output feature to the ones of BJTs. IGBTs are appropriate for excessive voltage .excessive cutting-edge ,and frequencies up to twenty KHZ.IGBT are to be had as much as 1700 V, 2400 A.

A SITs is a excessive- energy, excessive –frequency device. It is largely the solid-country model of the triode vacuum tube, and is much like a junction subject impact transistor. It has a low-noise, low distortion, excessive –audio frequency energy capability. The turn-on and turn-off instances are very short, normally 0.25 microseconds. The generally on-feature and the excessive on-country drop restriction its packages for standard energy conversions. The cutting-edge score of SITs may be as much as 1200V, 300A, and the switching velocity may be as excessive as 100KHZ.

Power semiconductor gadgets may be operated as switches through making use of manage indicators to the gate of thyristors, base of electricity transistors to the gate of electricity MOSFETs and to the gate of IGBTs. The required output voltage is received through various the conduction time of those gadgets. Fig. indicates the output voltage and manage traits of some electricity switching gadgets



Three input phase voltages.

A. *Characteristic of switching gadgets*

The feature of a really perfect transfer are as follows:
1) In the on-nation transfer is on,it ought to have (a) the abilty to hold a excessive ahead contemporary IF, tending to infinity, (b) a low on-nation ahead voltage drop VON, tending to zero; and (c) a low on-nation resistance RON , tending to zero. Low RON reasons low on-nation strength loss PON, those symbols are typically stated beneath Neath dc steady-nation conditions.
2) In the off-nation while transfer is off, it ought to have (a) the abilty to face up to a excessive ahead or opposite voltage VBR , tending to infinity; (b) a low off-nation leakage contemporary IOFF ,tending to zero ; and (c) a excessive off-nation resistance ROFF ,tending to infinity. Excessive ROFF motive low off-nation strength loss POFF .These symbols are typically stated beneath Neath dc steady- nation conditions.
3) During turn-on and turn-off processes, it ought to be absolutely became on and stale instantly in order that the gadgets may be operated at excessive frequencies. Thus, it ought to have (a) a low postpone time td, tending to zero; (b) a low upward thrust time tr, tending to zero; (c) a low garage time ts, tending to zero; and (d) a low fall time tf, tending to zero.
4) For turn –on and turn-off, it ought to require (a) a low gate power strength PG, tending to zero; (b)) a low gate power voltage VG, tending to zero; (c)) a low gate power contemporary IG, tending to zero.
5) Both turn-on and turn-off ought to be controllable. Thus, it ought to activate with a gate sign and ought to turn-off with any other gate sign.
6) For turning on and stale, it must require a pulse sign only, that is, a small pulse with a completely small width tw, tending to zero.
7) It ought to have a excessive dv/dt, tending to infinity. That is, the transfer ought to be able to managing fast modifications of the voltage throughout it.

B. Continuous conduction mode

If the modern-day via the inductor L in no way falls to 0 for the duration of a commutation cycle, the converter is stated to perform in non-stop mode. The modern-day and voltage waveforms in a perfect converter may be visible in Figure.

From $t=0$ to $t=DT$, the converter is in On-State, so the switch S is closed. The rate of change in the inductor current (I_L) is therefore given by

$$\frac{\mathrm{d}\,I_{\mathrm{L}}}{\mathrm{d}\,t}=\frac{V_i}{L}$$

At the end of the On-state, the increase of I_L is therefore:

$$\Delta I_{\mathrm{L_{On}}}=\int_0^{DT}\mathrm{d}\,I_{\mathrm{L}}=\int_0^{DT}\frac{V_i}{L}\,\mathrm{d}\,t=\frac{V_i\,D\,T}{L}$$

D is the obligation cycle. It represents the fraction of the commutation duration T at some stage in which the transfer is on. Therefore D levels among 0 (S is in no way on) and 1 (S is continually on).

During the Off-state, the transfer S is open, so the inductor present day flows thru the load. If we count on 0 voltage drop with inside the diode, and a capacitor huge sufficient for its voltage to stay constant, the evolution of IL is:

$$\frac{\mathrm{d}\,I_{\mathrm{L}}}{\mathrm{d}\,t}=\frac{V_o}{L}$$

Therefore, the variation of I_L during the Off-period is:

$$\Delta I_{\mathrm{L_{Off}}}=\int_0^{(1-D)T}\mathrm{d}\,I_{\mathrm{L}}=\int_0^{(1-D)T}\frac{V_o\,\mathrm{d}\,t}{L}=\frac{V_o\,(1-D)\,T}{L}$$

As we consider that the converter operates in steady-state conditions, the amount of energy stored in each of its components has to be the same at the beginning and at the end of a commutation cycle. As the energy in an inductor is given by:

$$E=\frac{1}{2}L\,I_{\mathrm{L}}^2$$

It is obvious that the value of I_L at the end of the off state must be the same with the value of I_L at the beginning of the On-state, i.e. the sum of the variations of I_L during the on and the off states must be zero:

$$\Delta I_{\mathrm{L_{On}}}+\Delta I_{\mathrm{L_{Off}}}=0$$

Substituting $\Delta I_{\mathrm{L_{On}}}$ and $\Delta I_{\mathrm{L_{Off}}}$ by their expressions yields:

$$\Delta I_{\mathrm{L_{On}}}+\Delta I_{\mathrm{L_{Off}}}=\frac{V_i\,D\,T}{L}+\frac{V_o(1-D)T}{L}=0$$

This can be written as:

$$\frac{V_o}{V_i}=\left(\frac{-D}{1-D}\right)$$

This in return yields that:

$$D=\frac{V_o}{V_o-V_i}$$

From the above expression it is able to be visible that the polarity of the output voltage is constantly negative (due to the fact the obligation cycle is going from zero to 1), and that its absolute price will increase with D, theoretically as much as minus infinity while D tactics 1. Apart from the polarity, this converter is both step-up (a lift converter) and step-down (a dollar converter). Thus it is known as a Boost converter.

1) DC-DC converters are used to transform one DC voltage to other. These converters are considerably utilized in enterprise in addition to in research. One of the primary boundaries of those converters is unregulated deliver of voltage and modern-day. To triumph over those troubles there are numerous manage strategies used in mixture with those converters. In this assessment we summarized few of those manage strategies. Some famous manage strategies are Voltage mode manage (VMC),
2) ACurrent mode manage (CMC) and PID. We have additionally paid interest at the blessings and downsides of those strategies with simple working principle.
3) DC-DC converters are one of the essential digital circuits, which can be broadly utilized in electricity electronics. The major trouble with operation of DC-DC converter is unregulated electricity deliver, which ends up in incorrect characteristic of DC –DC converters.

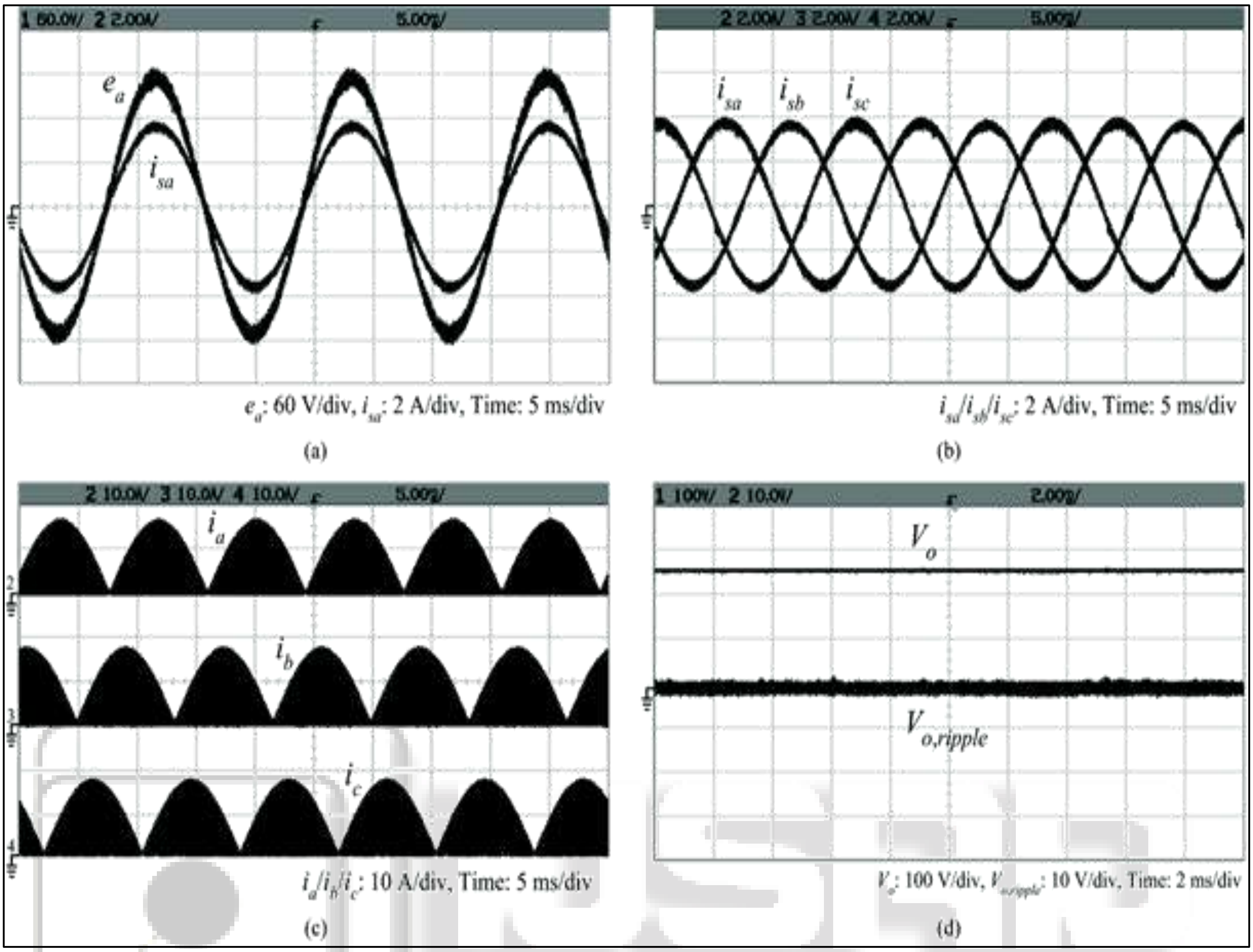
   There are numerous analogue and virtual manage methods used for dc-dc converters and some have been followed through enterprise which include voltage- and modern-day-mode manage strategies. The DC-DC converter inputs are generally unregulated dc voltage enter and the required outputs have to be a consistent or constant voltage.

   Application of a voltage regulator is that it have to keeps a consistent or constant output voltage regardless of variant in load modern-day or enter voltage. Various varieties of voltage regulators with numerous manage schemes are used to decorate the performance of DC-DC converters. Today due to the advancement in electricity electronics and improved technology a extra intense requirement for correct and dependable law is desired5. This has brought about want for extra superior and dependable layout of controller for dc-dc converters. There are numerous sorts of DC- DC converters required for specific cause like Buck, Boost, Buck and Boost, Cuk and flyback.

   These all DC- DC converters have their precise configurations to finish their tasks. Varieties in DC-DC converter required specific sort of controlling strategies due to the fact unmarried method can't be implemented to all converters because the all have specific specifications.

   Aim of this paper is to have a top level view of all of the manage strategies used to facilitate the overall performance of numerous varieties of DC-DC converters.

   We will in brief talk the simple concept, blessings and downside of every manage method for the duration of this assessment.
4) To observe the switching schemes of electricity switches. The non-inverting dollar raise converter may be operated at the 3 specific modes. When the enter voltage Vin is drastically better than the preferred output voltage Vout, the converter may fit with inside the dollar – mode. When Vin is decrease than Vout, it can paintings with inside the raise mode. - raise mode.



e_a: 60 V/div, i_{sa}: 2 A/div, Time: 5 ms/div (a)
$i_{sa}/i_{sb}/i_{sc}$: 2 A/div, Time: 5 ms/div (b)
$i_a/i_b/i_c$: 10 A/div, Time: 5 ms/div (c)
V_o: 100 V/div, $V_{o,ripple}$: 10 V/div, Time: 2 ms/div (d)

Three input phase voltages 120 lag Current mode manipulate of DC-DC converters

To observe the controlling schemes of dc-dc converters. They are classified into types- voltage mode manage and modern-day mode It is a sort of unmarried loop controller related to a reference voltage, so at the start output voltage is measured and as compared to a reference voltage (figure-1.5.1). This VMC technique is utilized in studies in addition to in enterprise due its clean implementation. It makes use of measured output and reference voltage to generate the manipulate voltage. After this the manipulate voltage is used to determine the switching duty ratio by comparison with a consistent frequency waveform. This duty ratio is used to hold the common voltage throughout the inductor. This will subsequently convey the output voltage to its reference price and which assist with inside the shipping of consistent voltage with none variation.

There are diverse drawbacks in VMC technique: i. Poor reliability and balance of the main switch. Ii. It is less in reliability, balance, or overall performance whilst numerous converters in parallel deliver with one load. Iii. It isn't easy and frequently inefficient strategies of maintaining the principle transformer of a push- pull converter running with inside the centre of its linear region. Iv. It is a very slow system, as respond is much longer of switching cycles.

All reaction depend upon the placement of comments loop as though the comments loop is closed, the inductor modern-day is proportional to the manipulate sign ic and the output voltage turns into same to reference voltage Vref. Irrespective of the numerous benefits of CMC it additionally has a few drawbacks as:

Advantages of modern-day mode manipulate techniques: i It suggests the stepped forward brief reaction as from the begging it reduces the order of the converter to a first order system. Ii. Good and stepped forward overall performance in line regulation. Iii. It is greater appropriate for converters working in parallel. Iv. Overload is hostile with the aid of using self-protection. V. Main transfer undertake greater protection. Vi. Main transformer middle is gift with inside the centre of its B-H curve as a end result of Anti-saturation. Vii. Disadvantages of modern-day mode manipulate techniques. Viii. It is very unstable while duty ratio exceeds 0.five with inside the height modern-day mode- manipulate. Ix. Presence of Sub-harmonic oscillations.

5) To look at the loss evaluation of energy switches. The realistic switching gadgets dissipates a few strength while accomplishing and switching. Voltage drop throughout a accomplishing energy tool is as a

minimum at the order of 1V, however regularly be higher, as much as numerous volts. The purpose of any new tool is to enhance the restrictions imposed with the aid of using the switching parameters. The common accomplishing energy loss ,PON is given with the aid of using PON =Where Ts denotes the conduction length and p is the instant energy loss (i.E ,made from voltage drop vsw throughout the transfer and performed modern-day isw). energy losses growth in the course of turn-on and turn-off of the transfer due to the fact in the course of the transistion from one conduction country to some other country each voltage and modern-day have massive values. The ensuing switching energy loss PSW in the course of the turn-on and turn-off periods ,is given with the aid of using Psw = fs ( )fs = 1/Ts is the switching frequency ;tr ,ts and tf are the upward thrust time , garage time and fall time respectively. Therefore ,the energy dissipation of a switching tool is given with the aid of using;

PD = PON +PSW +PG

in which PG is the gate driving force energy.

6) To look at the ACM non–inverting greenback- improve converter. On this approach simplest energy switches are extrade states in every switching cycle, as examine to four energy switches in a traditional greenback improve switching scheme. On this manner each the switching loss and conduction loss may be decreased effectively.
7) To look at the common modern-day mode (ACM) to govern and resolve the noise issue. Therefore, there are numerous advntages of the use of the ACM manipulate, the ACM manipulate is usually utilized in energy thing correction application. As referred to above, the proposed converter most effective works with inside the raise and dollar modes. In fact, the analyses in those modes are similar, and, hence, the evaluation of the ACM small-sign fashions for the converter running with inside the dollar mode is supplied beneath as an example. Fig. five indicates the simplified block diagram of the ACM non-inverting dollar-raise converter working with inside the dollar mode. There are loops and compensators on this system. The output voltage (Vout) is divided to βVout, and despatched to the voltage-loop compensator. Then, the output sign of the voltage-loop compensator (VC1) and the sensed inductor contemporary are despatched to the compensator of the contemporary loop. Finally, the output sign of the internal contemporary-loop compensator (VC2) is as compared with the ramp sign to decide the obligation ratio.

In this system, small-sign switch capabilities associated with the energy degree are required to layout the contemporary-loop compensator: the control-to-output characteristic Gvc-dollar(s) and the control-to-inductor-contemporary characteristic Gic-dollar (s) of the contemporary loop. They may be expressed as

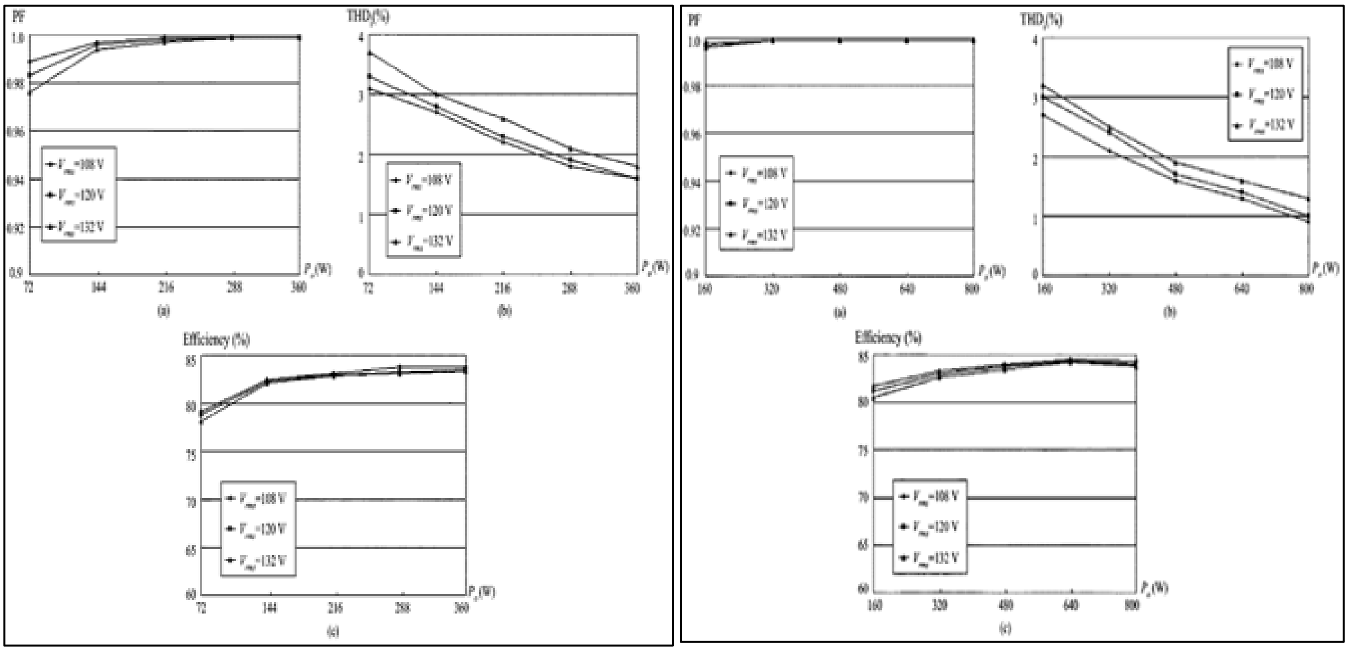
IV. EXPERIMENTAL RESULTS

The dc–dc dollar–improve converter proven in Fig. 1 is imple- mented below exceptional operational modes to show the validity of the proposed theoretical subjects. The principal parameters are taken into consideration as Vi = 15 − 20 V, RL = 50 − 250 Ω, f = 10 kHz, and VPP = %2Vo. The voltage levels, switching frequency, and different electric parameters of the converter were decided on in experimental ranges. In addition, every other cause of select- ing such numerical values for the electric portions of the converter is thinking about a pattern of software of those styles of converters (mine manipulate system, petrochemical, refinery), which had been referred in [15]. The transistor (transfer S) and diode (D) of the prototype are MJ13005 (the traits of this transistor is identical with BUT11) and MUR460, respectively [18]–[20]. The 89C52 microcontroller via way of means of ATMEL Company has been used to generate the switching pattern. Available dc energy components in laboratories have supplied the desired dc voltage source.

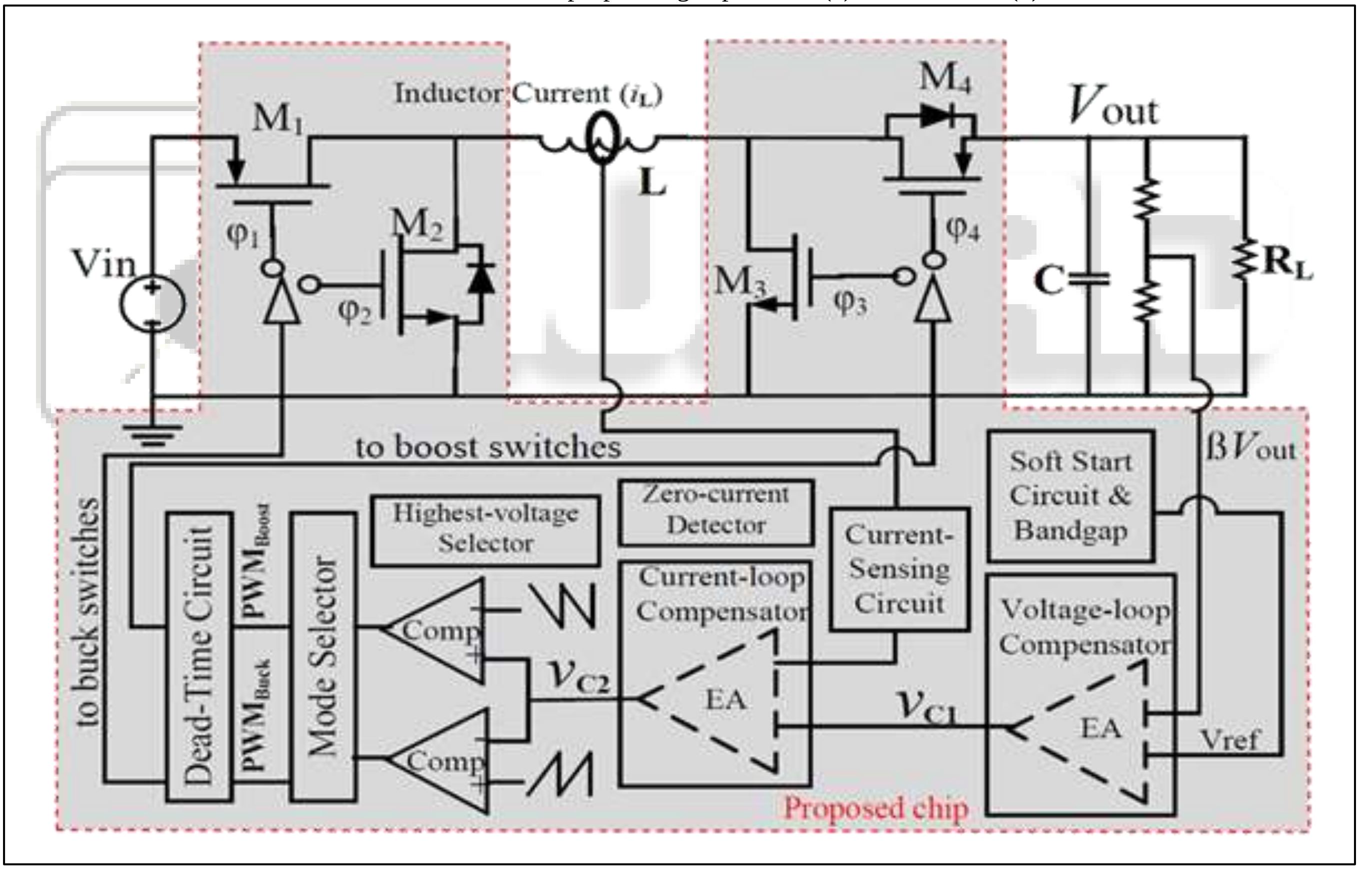
The experimental outcomes are given one at a time for 2 dollar and improve operations. The converter parameters for the improve situation are taken into consideration as

Vo = −25 V, Vi = 17 V, RL = one hundred Ω, and Io = 0.25 A. Considering (14)–(20) and (68), the values of LC , LK , LC,min , LC,max , LK,min , LK,max , and C are received as 0.eighty two mH, 1.38 mH, 0.35 mH, 2.forty seven mH, 0.fifty six mH, 4.forty four mH, and 62.five µF, respectively.

The inductor modern-day and output voltage waveforms are proven in Fig. 10, thinking about exceptional inductance values. As proven in Fig. 10(a), if L = 0.6 mH is decided on, he converter operates in IISM-DCM due to the fact it's miles decrease than LC = 0.eighty two mH. Fig. 10(a) indicates that the minimal inductor modern-day is much less than the burden modern-day which confirms the theoretical evaluation proven in Fig.



Some measured results for step-up voltage operation. (a) Power factor. (b) THD



Block diagram of the ACM non-inverting buck-boost converter

REFERENCES

[1] B. Sahu, and G. A. Rincón-Mora, "A low voltage, dynamic, noninverting, synchronous buck-boost converter for portable applications," IEEE Trans. Power Electron., vol. 19, no. 2, pp. 443-452, 2004.

[2] M. H. Huang and K. H. Chen, "Single-inductor multi-output (SIMO) dc-dc converters with high light-load efficiency and minimized cross-regulation for portable device," IEEE Journal of Solid-Sate Circuits, vol. 44, no. 4, pp. 1099-1111, Apr. 2009.

[3] B. Sahu and G. A. Rincon-Mora "A high-efficiency, dual-mode, dynamic, buck-boost power supply IC for portable applications," in Proc. IEEE 18th international conference on VLSI Design, 2005, pp. 858 – 861.

[4] Y. H. Lee, S. C. Huang, S. W. Wang, W. C. Wu, P. C. Hung, H. H. Ho, Y. T. Lai, and K. H. Chen, "Power-

tracking embedded buck-boost converter with fast dynamic voltage scaling for SoC system," IEEE Trans. Power Electron., vol. 27, no. 3, pp. 1270-1282, 2012.

[5] M. H. Huang and K. H. Chen, "Single-inductor dual buck-boost output (SIDBBO) converters with adaptive current control mode (ACCM) and adaptive body switch (ABS) for compact size and long battery life in

[6] P. C. Huang, W. Q. Wu, H. H. Ho, K. H. Chen, and G. K. Ma, "High efficiency buck-boost converter with reduced average inductor current (RAIC) technique," in Proc. IEEE European Solid-State Circuits Conf., ESSCIRC, 2009, pp. 456-459.

[7] Y. J. Lee, A. Khaligh, and A. Emadi, "A compensation technique for smooth transitions in a noninverting buck–boost converter," IEEE Trans. Power Electron., vol. 24, no. 4, pp. 1002-1116, 2009.

[8] C. Restrepo, J. Calvente, A. Cid-Pastor, A. E. Aroudi, and R. Giral, "A noninverting buck-boost dc-dc switching converter with high efficiency and wide bandwidth," IEEE Trans. Power Electron., vol. 26, no. 9, pp.2490-2503, 2011.

[9] C. Yao, X. Ruan, X. Wang, and C. K. Tse, "Isolated buck-boost DC/DC converters suitable for wide input-voltage range," IEEE Trans. Power Electron., vol. 26, no. 9, pp. 2599-2613, 2011.

[10] B. Sahu, Integrated, dynamically adaptive supplies for linear RF power amplifiers in portable applications, doctoral dissertation, Georgia Institute of Technology, 2004

[11] C. C. Chang and C. L. Wei, "Single-inductor four-switch non-inverting buck-boost dc-dc converter," in Proc. IEEE International Symposium on VLSI Design, Automation and Test, 2011, pp. 277-280.

[12] P. Midya, K. Haddad, and M. Miller, "Buck or boost tracking power converter," IEEE Power Electron. Lett. vol. 2, no. 4, pp. 131-134, Dec.

[13] J. A. Morales-Saldana, J. Leyva-Ramos, E. E. Carbajal-Gutierrez, and M. G. Ortiz-Lopez, "Average current-mode control scheme for a quadratic buck converter with a single switch," IEEE Trans.Power Electron., vol. 23, no. 1, pp. 485-490, 2008.

[14] W. Tang, F. C. Lee, and R. B. Ridley, "Small-signal modeling of average current-mode control," IEEE Trans. Power Electron., vol.8, no.2, pp. 112-119, 1993.

[15] L. H. Dixon, "Average current-mode control of switching power supplies," Unitrode power supply design seminar manual, 1990.

[16] J. Sun and R. M. Bass. "Modeling and practical design issues forth average current control," in Proc. 15IEEE Appl. P ower Electron. Conf.Expo, APEC, 1999, vol.2, pp. 980-986.

[17] A. M. Roslan, K. H. Ahmed, S. J. Finney, and B. W. Williams, "Improved instantaneous average current-sharing control scheme for parallel-connected inverter considering line impedance impact in microgrid networks," IEEE Trans. Power Electron., vol. 26, no. 3, pp.702-716, 2011.

[18] X. Du, L. Zhou, and H. M. Tai, "Average current control of a series-type single-phase PFC with hybrid modulation," IEEE Trans. Power Electron., vol. 26, no. 9, pp. 2381-2385, 2011.

[19] R. Paul and D. Maksimovic, "Analysis of PWM nonlinearity in non-inverting buck-boost power converters," in Proc.39 Power Electron. Spec. Conf., PESC, 2008, pp. 3741-3747.